



# Dual, Triple, and Quad Standard-Definition Video Filter Amplifiers with DC-Coupled Input Buffers

## General Description

The MAX9583/MAX9584/MAX9585 are small, low-power, multichannel video amplifiers with integrated reconstruction filters. Specially suited for standard-definition video signals, these devices are ideal for a wide range of television, set-top box, and portable applications.

The MAX9583/MAX9584/MAX9585 inputs can be directly connected to the outputs of a video digital-to-analog converter (DAC). The reconstruction filter typically has  $\pm 1\text{dB}$  passband flatness at 8.5MHz and 55dB attenuation at 27MHz. The amplifiers have a 2V/V gain and the outputs can be DC-coupled to a 75 $\Omega$  load which is the equivalent of two video loads, or AC-coupled to a 150 $\Omega$  load.

The MAX9583/MAX9584/MAX9585 operate from a 2.7V to 3.6V single supply and are specified over the  $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$  automotive temperature range. The MAX9583 is offered in a small, 6-pin thin SOT23 package. The MAX9584 is offered in a small, 8-pin  $\mu\text{MAX}^{\text{®}}$  package, and the MAX9585 is offered in a small, 10-pin  $\mu\text{MAX}$  package.

## Applications

Set-Top Boxes  
Televisions  
Portable

## Features

- ◆ Dual- (MAX9583), Triple- (MAX9584), and Quad- (MAX9585) Channel Devices
- ◆ 8.5MHz,  $\pm 1\text{dB}$  Passband
- ◆ 55dB Attenuation at 27MHz
- ◆ Fixed Gain of 2V/V
- ◆ Low Power: 3.5mA per Channel
- ◆ 2.7V to 3.6V Single-Supply Operation
- ◆ Small SOT23 and  $\mu\text{MAX}$  Packages

## Ordering Information

| PART         | PIN-PACKAGE            | CHANNELS | PKG CODE |
|--------------|------------------------|----------|----------|
| MAX9583AZT+T | 6 Thin SOT23-6         | 2        | Z6+1     |
| MAX9584AUA+T | 8 $\mu\text{MAX}$ -8   | 3        | U8+1     |
| MAX9585AUB+T | 10 $\mu\text{MAX}$ -10 | 4        | U10+2    |

**Note:** All devices are specified over the  $-40^{\circ}\text{C}$  to  $+125^{\circ}\text{C}$  operating temperature range.

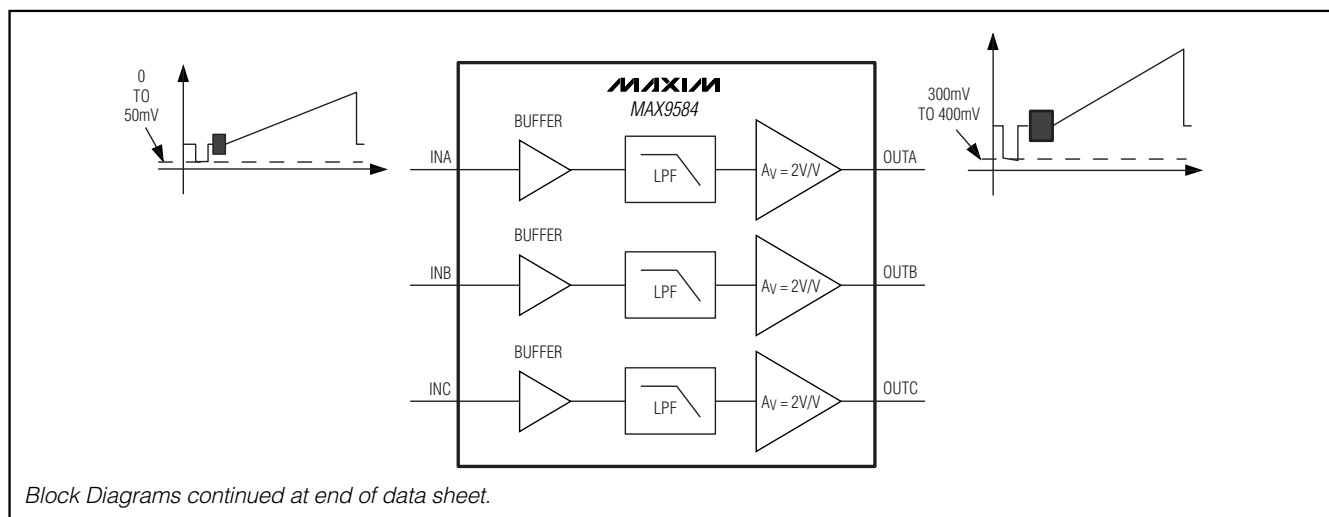
+ Denotes a lead-free package.

T = Tape and reel.

**Pin Configurations and Selector Guide located at end of data sheet.**

$\mu\text{MAX}$  is a registered trademark of Maxim Integrated Products, Inc.

## Block Diagrams



# Dual, Triple, and Quad Standard-Definition Video Filter Amplifiers with DC-Coupled Input Buffers

## ABSOLUTE MAXIMUM RATINGS

|                                                                        |              |                                                      |                 |
|------------------------------------------------------------------------|--------------|------------------------------------------------------|-----------------|
| V <sub>DD</sub> to GND .....                                           | -0.3V to +4V | 8-Pin $\mu$ MAX (derate 4.5mW/°C above +70°C) .....  | 362mW           |
| IN <sub>-</sub> to GND .....                                           | -0.3V to +4V | 10-Pin $\mu$ MAX (derate 5.6mW/°C above +70°C) ..... | 444mW           |
| OUT <sub>-</sub> Short-Circuit Duration to V <sub>DD</sub> , GND ..... | Continuous   | Operating Temperature Range .....                    | -40°C to +125°C |
| Continuous Input Current                                               |              | Junction Temperature .....                           | +150°C          |
| IN <sub>-</sub> .....                                                  | ±20mA        | Storage Temperature Range .....                      | -65°C to +150°C |
| Continuous Power Dissipation (T <sub>A</sub> = +70°C)                  |              | Lead Temperature (soldering, 10s) .....              | +300°C          |
| 6-Pin Thin SOT23 (derate 9.1mW/°C above +70°C) ....                    | 727mW        |                                                      |                 |

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## ELECTRICAL CHARACTERISTICS

(V<sub>DD</sub> = 3.3V, GND = 0V, R<sub>L</sub> = no load, T<sub>A</sub> = T<sub>MIN</sub> to T<sub>MAX</sub>, unless otherwise noted. Typical values are at T<sub>A</sub> = +25°C) (Note 1)

| PARAMETER                    | SYMBOL           | CONDITIONS                                                                                                            |                                                         | MIN   | TYP   | MAX   | UNITS            |
|------------------------------|------------------|-----------------------------------------------------------------------------------------------------------------------|---------------------------------------------------------|-------|-------|-------|------------------|
| Supply Voltage Range         | V <sub>DD</sub>  | Guaranteed by PSRR                                                                                                    |                                                         | 2.7   |       | 3.6   | V                |
| Supply Current               | I <sub>DD</sub>  | Per channel                                                                                                           |                                                         |       | 3.5   | 7     | mA               |
| Input Voltage Range          | V <sub>IN</sub>  | Guaranteed by DC voltage gain                                                                                         | V <sub>DD</sub> = 2.7V                                  | 0     |       | 1.05  | V <sub>P-P</sub> |
|                              |                  |                                                                                                                       | V <sub>DD</sub> = 3V                                    | 0     |       | 1.2   |                  |
| Input Current                | I <sub>IN</sub>  | V <sub>IN</sub> = 0V                                                                                                  |                                                         |       | 0.6   | 10    | μA               |
| Input Resistance             | R <sub>IN</sub>  |                                                                                                                       |                                                         |       | 25    |       | MΩ               |
| DC Voltage Gain (Note 2)     | A <sub>v</sub>   | R <sub>L</sub> = 150Ω to GND                                                                                          | V <sub>DD</sub> = 2.7V,<br>0V ≤ V <sub>IN</sub> ≤ 1.05V | 1.92  | 2     | 2.04  | V/V              |
|                              |                  |                                                                                                                       | V <sub>DD</sub> = 3V,<br>0V ≤ V <sub>IN</sub> ≤ 1.2V    | 1.92  | 2     | 2.04  |                  |
| DC Gain Matching             |                  | Guaranteed by DC voltage gain                                                                                         |                                                         | -2    | 0     | +2    | %                |
| Output Level                 |                  | Measured at V <sub>OUT</sub> , V <sub>IN</sub> = 0V, R <sub>L</sub> = 150Ω to GND                                     |                                                         | 0.210 | 0.300 | 0.410 | V                |
| Output-Voltage Swing         |                  | Measured at output, V <sub>DD</sub> = 2.7V, 0V ≤ V <sub>IN</sub> ≤ 1.05V, R <sub>L</sub> = 150Ω to -0.2V              |                                                         |       | 2.1   |       | V <sub>P-P</sub> |
|                              |                  | Measured at output, V <sub>DD</sub> = 2.7V, 0V ≤ V <sub>IN</sub> ≤ 1.05V, R <sub>L</sub> = 150Ω to V <sub>DD</sub> /2 |                                                         |       | 2.1   |       |                  |
|                              |                  | Measured at output, V <sub>DD</sub> = 3V, 0V ≤ V <sub>IN</sub> ≤ 1.2V, R <sub>L</sub> = 150Ω to -0.2V                 |                                                         |       | 2.4   |       |                  |
|                              |                  | Measured at output, V <sub>DD</sub> = 3V, 0V ≤ V <sub>IN</sub> ≤ 1.2V, R <sub>L</sub> = 150Ω to V <sub>DD</sub> /2    |                                                         |       | 2.4   |       |                  |
|                              |                  | Measured at output, V <sub>DD</sub> = 3.135V, 0V ≤ V <sub>IN</sub> ≤ 1.05V, R <sub>L</sub> = 75Ω to -0.2V             |                                                         |       | 2.1   |       |                  |
| Output Short-Circuit Current |                  | Short to GND (sourcing)                                                                                               |                                                         |       | 140   |       | mA               |
|                              |                  | Short to V <sub>DD</sub> (sinking)                                                                                    |                                                         |       | 70    |       |                  |
| Output Resistance            | R <sub>OUT</sub> | V <sub>OUT</sub> = 1.5V, -10mA ≤ I <sub>LOAD</sub> ≤ 10mA                                                             |                                                         |       | 0.2   |       | Ω                |
| Power-Supply Rejection Ratio | PSRR             | 2.7V ≤ V <sub>DD</sub> ≤ 3.6V                                                                                         |                                                         | 48    |       |       | dB               |
|                              |                  | f = 1MHz, 100mV <sub>P-P</sub>                                                                                        |                                                         |       | 29    |       |                  |

# Dual, Triple, and Quad Standard-Definition Video Filter Amplifiers with DC-Coupled Input Buffers

## ELECTRICAL CHARACTERISTICS (continued)

( $V_{DD} = 3.3V$ ,  $GND = 0V$ ,  $R_L = \text{no load}$ ,  $T_A = T_{MIN}$  to  $T_{MAX}$ , unless otherwise noted. Typical values are at  $T_A = +25^\circ C$ ) (Note 1)

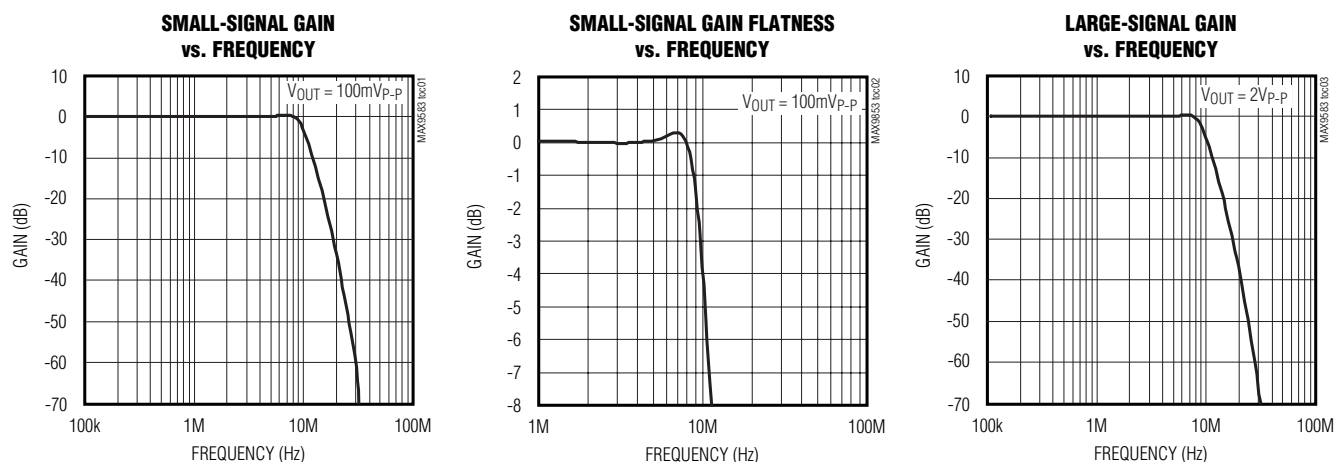
| PARAMETER                                 | SYMBOL | CONDITIONS                                                                                                             | MIN                 | TYP   | MAX | UNITS    |
|-------------------------------------------|--------|------------------------------------------------------------------------------------------------------------------------|---------------------|-------|-----|----------|
| Standard-Definition Reconstruction Filter |        | $\pm 1\text{dB}$ passband flatness                                                                                     |                     | 8.5   |     | MHz      |
|                                           |        | $V_{IN} = 1V_{P-P}$ , reference frequency is 100kHz                                                                    | $f = 5.5\text{MHz}$ | -0.15 |     | dB       |
|                                           |        |                                                                                                                        | $f = 9.5\text{MHz}$ | -3    |     |          |
|                                           |        |                                                                                                                        | $f = 27\text{MHz}$  | -55   |     |          |
| Differential Gain                         | DG     | 5-step modulated staircase of 129mV step size and 286mV peak-to-peak subcarrier amplitude, $f = 4.43\text{MHz}$        |                     | 0.1   |     | %        |
| Differential Phase                        | DP     | 5-step modulated staircase of 129mV step size and 286mV peak-to-peak subcarrier amplitude, $f = 4.43\text{MHz}$        |                     | 0.4   |     | Degrees  |
| 2T Pulse-to-Bar K Rating                  |        | $2T = 200\text{ns}$ , bar time is $18\mu\text{s}$ . The beginning 2.5% and the ending 2.5% of the bar time are ignored |                     | 0.6   |     | K%       |
| 2T Pulse Response                         |        | $2T = 200\text{ns}$                                                                                                    |                     | 0.2   |     | K%       |
| 2T Bar Response                           |        | $2T = 200\text{ns}$ , bar time is $18\mu\text{s}$ . The beginning 2.5% and the ending 2.5% of the bar time are ignored |                     | 0.2   |     | K%       |
| Nonlinearity                              |        | 5-step staircase                                                                                                       |                     | 0     |     | %        |
| Group Delay Distortion                    |        | $100\text{kHz} \leq f \leq 5.5\text{MHz}$ , outputs are $2V_{P-P}$                                                     |                     | 9     |     | ns       |
| Peak Signal to RMS Noise                  |        | $100\text{kHz} \leq f \leq 5.5\text{MHz}$                                                                              |                     | 71    |     | dB       |
| Output Impedance                          |        | $f = 5.5\text{MHz}$                                                                                                    |                     | 4.8   |     | $\Omega$ |
| All Hostile Crosstalk                     |        | $f = 4.43\text{MHz}$                                                                                                   |                     | -64   |     | dB       |

**Note 1:** All devices are 100% production tested at  $T_A = +25^\circ C$ . Specifications over temperature limits are guaranteed by design.

**Note 2:** Voltage gain ( $A_V$ ) is a two-point measurement in which the output voltage swing is divided by the input voltage swing.

## Typical Operating Characteristics

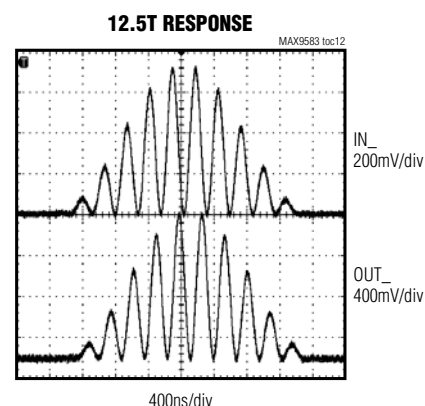
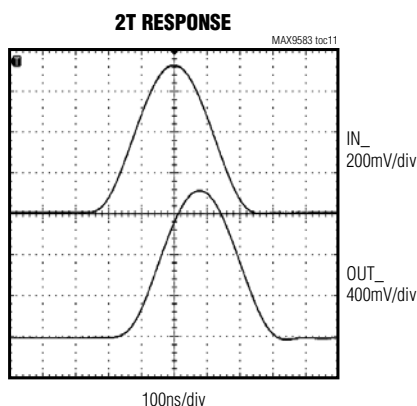
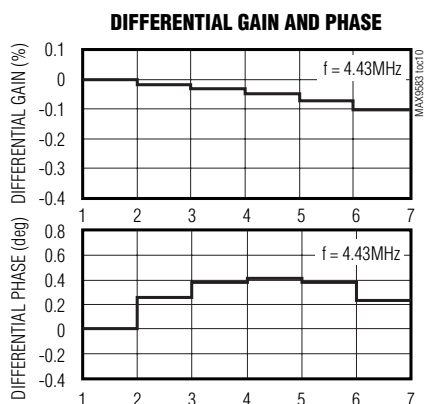
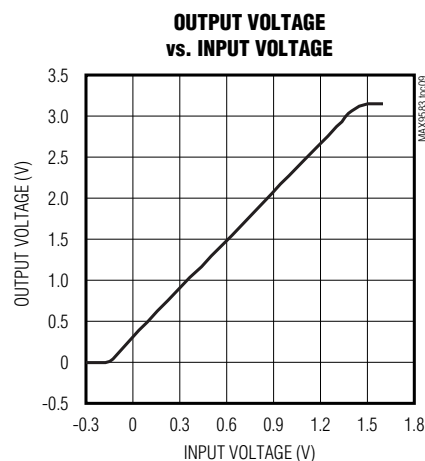
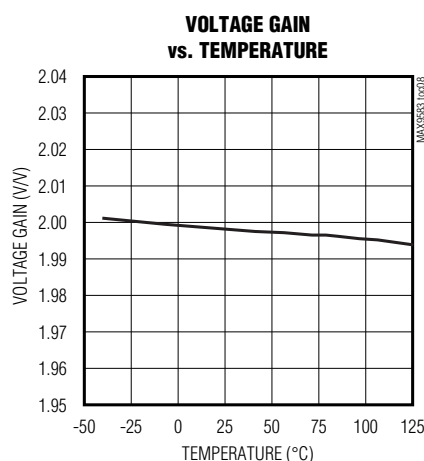
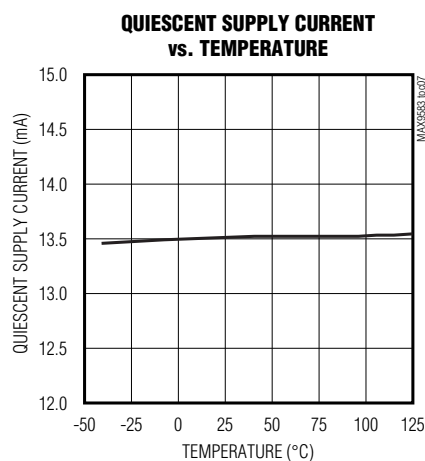
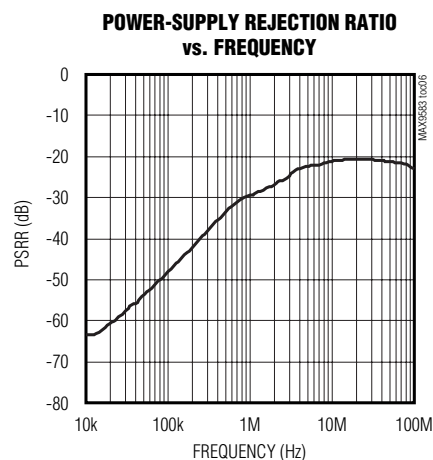
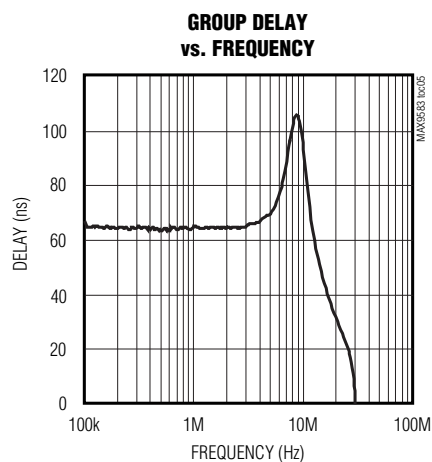
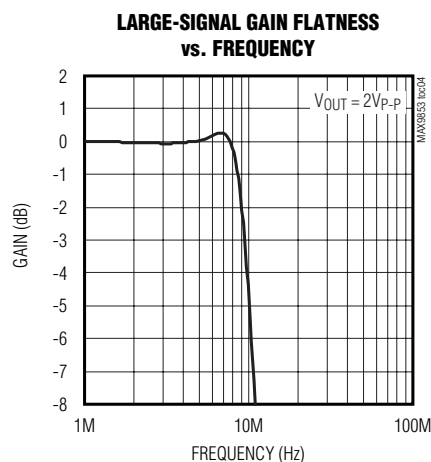
( $V_{DD} = \overline{\text{SHDN}} = 3.3V$ , video outputs have  $R_L = 150\Omega$  connected to GND,  $T_A = +25^\circ C$ , unless otherwise noted.)



# Dual, Triple, and Quad Standard-Definition Video Filter Amplifiers with DC-Coupled Input Buffers

## Typical Operating Characteristics (continued)

( $V_{DD} = \overline{\text{SHDN}} = 3.3\text{V}$ , video outputs have  $R_L = 150\Omega$  connected to GND,  $T_A = +25^\circ\text{C}$ , unless otherwise noted.)

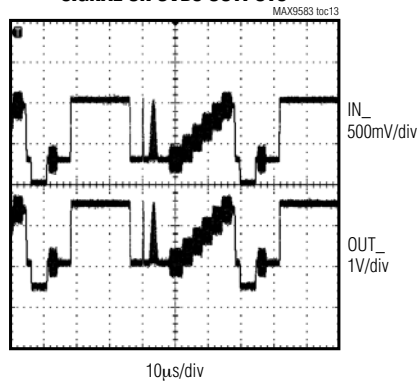


# Dual, Triple, and Quad Standard-Definition Video Filter Amplifiers with DC-Coupled Input Buffers

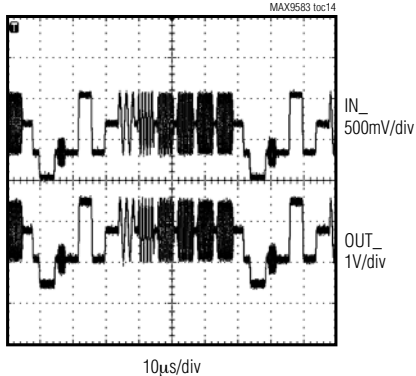
## Typical Operating Characteristics (continued)

( $V_{DD} = \overline{SHDN} = 3.3V$ , video outputs have  $R_L = 150\Omega$  connected to GND,  $T_A = +25^\circ C$ , unless otherwise noted.)

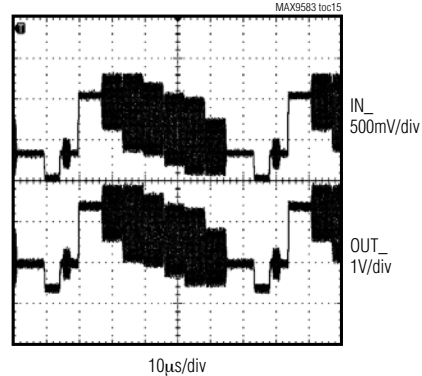
**NTC-7 VIDEO TEST  
SIGNAL ON CVBS OUTPUTS**



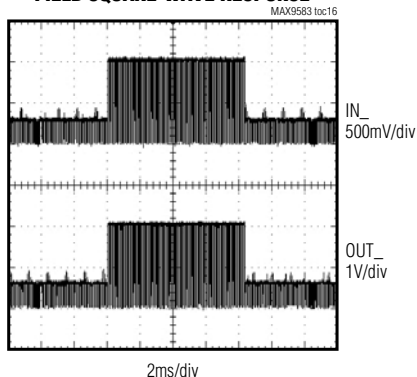
**PAL MULTIBURST RESPONSE**



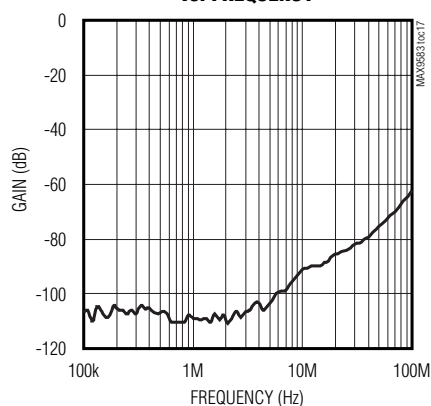
**PAL COLOR BARS**



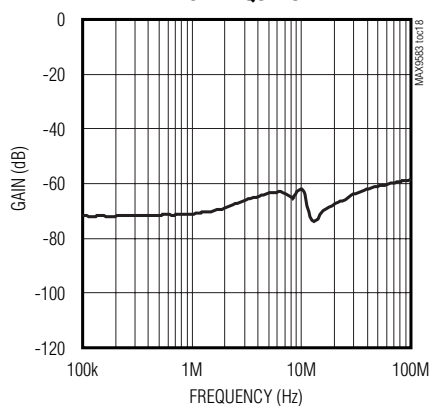
**FIELD SQUARE-WAVE RESPONSE**



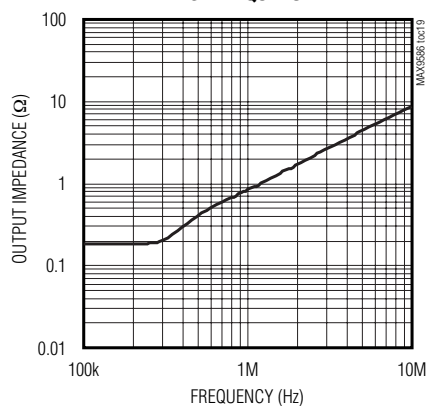
**INPUT-TO-INPUT CROSSTALK  
vs. FREQUENCY**



**OUTPUT-TO-OUTPUT CROSSTALK  
vs. FREQUENCY**



**OUTPUT IMPEDANCE  
vs. FREQUENCY**

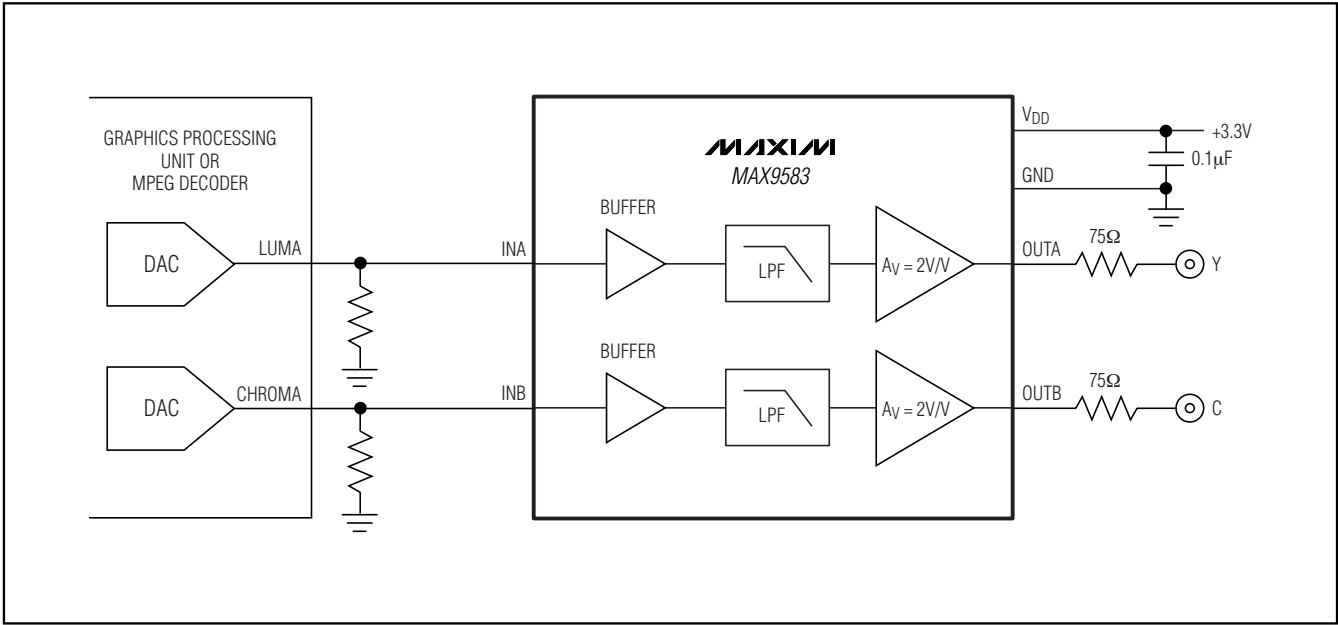


# Dual, Triple, and Quad Standard-Definition Video Filter Amplifiers with DC-Coupled Input Buffers

## Pin Description

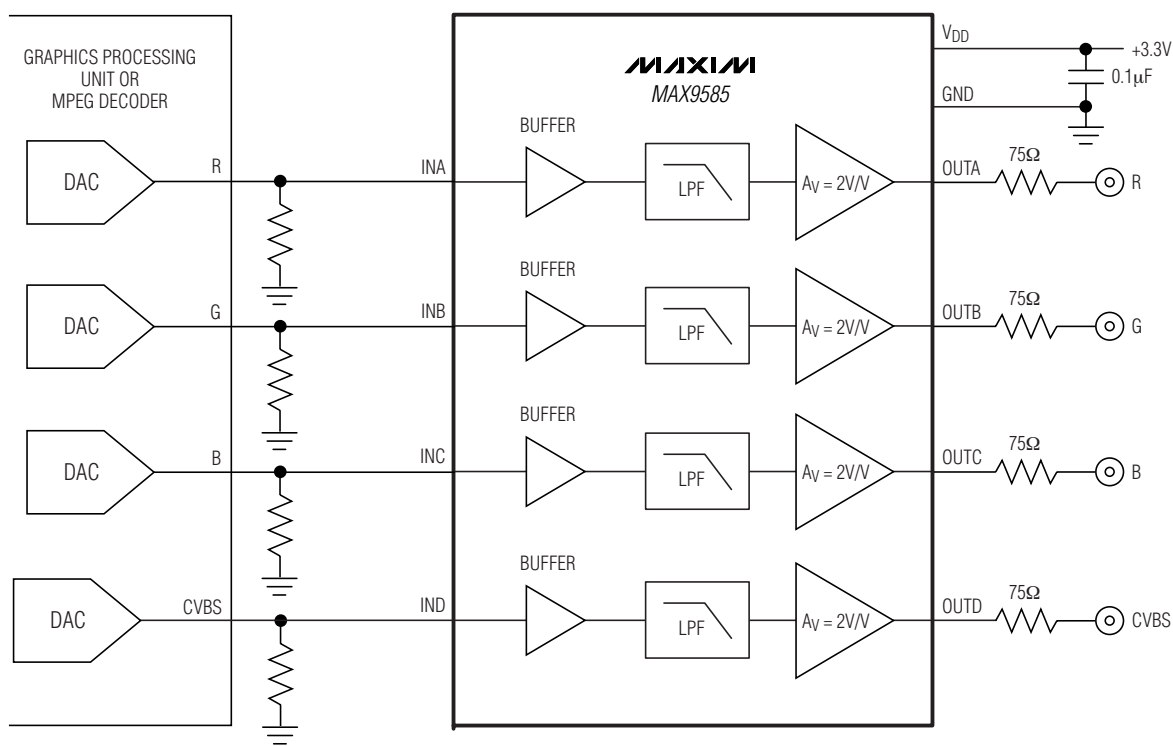
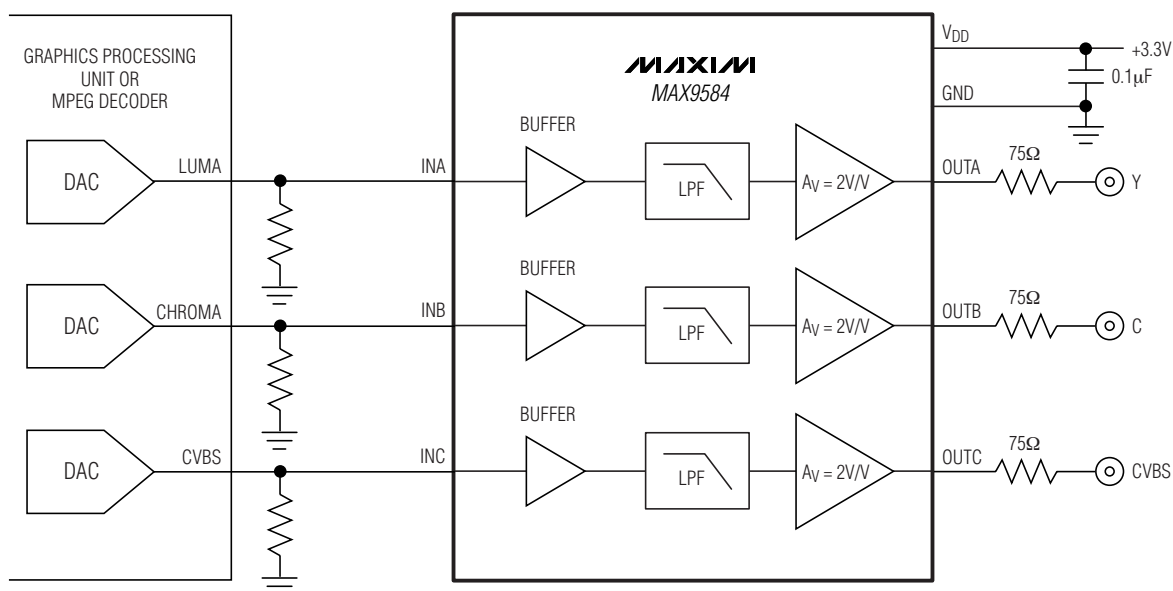
| MAX9583 | MAX9584     | MAX9585      | NAME            | FUNCTION                                                           |
|---------|-------------|--------------|-----------------|--------------------------------------------------------------------|
| 6 SOT23 | 8 $\mu$ MAX | 10 $\mu$ MAX |                 |                                                                    |
| 2       | 4           | 5            | GND             | Ground                                                             |
| 3       | 1           | 1            | INA             | Video Input A                                                      |
| 1       | 2           | 2            | INB             | Video Input B                                                      |
| —       | 3           | 3            | INC             | Video Input C                                                      |
| —       | —           | 4            | IND             | Video Input D                                                      |
| 4       | 7           | 9            | OUTA            | Video Output A                                                     |
| 6       | 6           | 8            | OUTB            | Video Output B                                                     |
| —       | 5           | 7            | OUTC            | Video Output C                                                     |
| —       | —           | 6            | OUTD            | Video Output D                                                     |
| 5       | 8           | 10           | V <sub>DD</sub> | Positive Power Supply. Bypass to GND with a 0.1 $\mu$ F capacitor. |

## Typical Application Circuits



# Dual, Triple, and Quad Standard-Definition Video Filter Amplifiers with DC-Coupled Input Buffers

## Typical Application Circuits (continued)



MAX9583/MAX9584/MAX9585

# Dual, Triple, and Quad Standard-Definition Video Filter Amplifiers with DC-Coupled Input Buffers

## Detailed Description

The MAX9583/MAX9584/MAX9585 filter and amplify the video DAC output in applications such as set-top boxes and televisions. These devices consist of input buffers, lowpass filters, and gain of 2V/V output amplifiers capable of driving a standard 150 $\Omega$  video load to ground.

### Inputs

The video inputs should be directly connected to the output of the video current DAC. DC coupling ensures that the input signals are ground referenced so that the sync tip of composite or luma signals is within 50mV of ground and the blank level of the chroma signal is between 0.5V and 0.65V. Since the input buffers are identical, any standard-definition video signal can be applied to those inputs provided the signal is between ground and 1.05V when  $V_{DD} = 2.7V$ . For example, three composite video signals could be applied to INA, INB, and INC of the MAX9584. The RGB set or the YPbPr set can also be inputs to INA, INB, and INC of the MAX9584.

### Video Filter

The filter passband ( $\pm 1dB$ ) is typically 8.5MHz, which makes the device suitable for standard-definition video signals from all sources (e.g., broadcast and DVD). Broadcast video signals are channel limited: NTSC signals have 4.2MHz bandwidth and PAL signals have 5MHz bandwidth. Video signals from a DVD player, however, are not channel limited, so the bandwidth of DVD video signals can approach the Nyquist limit of 6.75MHz. (Recommendation ITU-R BT.601-5 specifies 13.5MHz as the sampling rate for standard-definition video). Therefore, the maximum bandwidth of the signal is 6.75MHz. To ease the filtering requirements, most modern video systems oversample by two times, clocking the video current DAC at 27MHz.

### Outputs

The video output amplifiers can both source and sink load current, allowing output loads to be DC- or AC-coupled. The amplifier output stage needs approximately 300mV of headroom from either supply rail. The devices have an internal level-shift circuit that positions the sync tip at approximately 300mV at the output. The blank level of the chroma output is positioned at approximately 1.3V if the blank level of the chroma input signal is 0.5V. The blank level of the chroma output is positioned at approximately 1.5V if the blank level of the chroma input signal is 0.6V.

If the supply voltage is greater than 3.135V (5% below a 3.3V supply), each amplifier can drive two DC-coupled video loads to ground. If the supply is less than 3.135V, each amplifier can drive only one DC-coupled or AC-coupled video load.

## Applications Information

### Reducing Power Consumption in the Video DACs

The MAX9583/MAX9584/MAX9585 have high-impedance input buffers that work with source resistances as high as 1000 $\Omega$ . To reduce power dissipation in the video DACs, the DAC output resistor can be scaled up in value. The reference resistor that sets the reference current inside the video DACs must also be similarly scaled up. For instance, if the output resistor is 37.5 $\Omega$ , the DAC must source 26.7mA when the output is 1V. If the output resistor is increased to 300 $\Omega$ , then the DAC only needs to source 3.33mA when the output is 1V.

There is parasitic capacitance from the DAC output to ground. That capacitance, in parallel with the DAC output resistor, forms a pole that can potentially roll off the frequency response of the video signal. For example, 300 $\Omega$  in parallel with 50pF create a pole at 10.6MHz. To minimize this capacitance, reduce the area of the signal trace attached to the DAC output as much as possible, and place the MAX9583/MAX9584/MAX9585 as close as possible to the video DAC outputs.

### AC-Coupling the Outputs

The outputs can be AC-coupled since the output stage can source and sink current as shown in Figure 1. Coupling capacitors should be 220 $\mu F$  or greater to keep the highpass filter, formed by the 150 $\Omega$  equivalent resistance of the video transmission line, to a corner frequency of 4.8Hz or below. The frame rate of PAL systems is 25Hz, and the frame rate of NTSC systems is 30Hz. The corner frequency should be well below the frame rate.

### Power-Supply Bypassing and Ground

The MAX9583/MAX9584/MAX9585 operate from a single-supply voltage down to 2.7V, allowing for low-power operation. Bypass  $V_{DD}$  to GND with a 0.1 $\mu F$  capacitor. Place all external components as close as possible to the device.

# Dual, Triple, and Quad Standard-Definition Video Filter Amplifiers with DC-Coupled Input Buffers

MAX9583/MAX9584/MAX9585

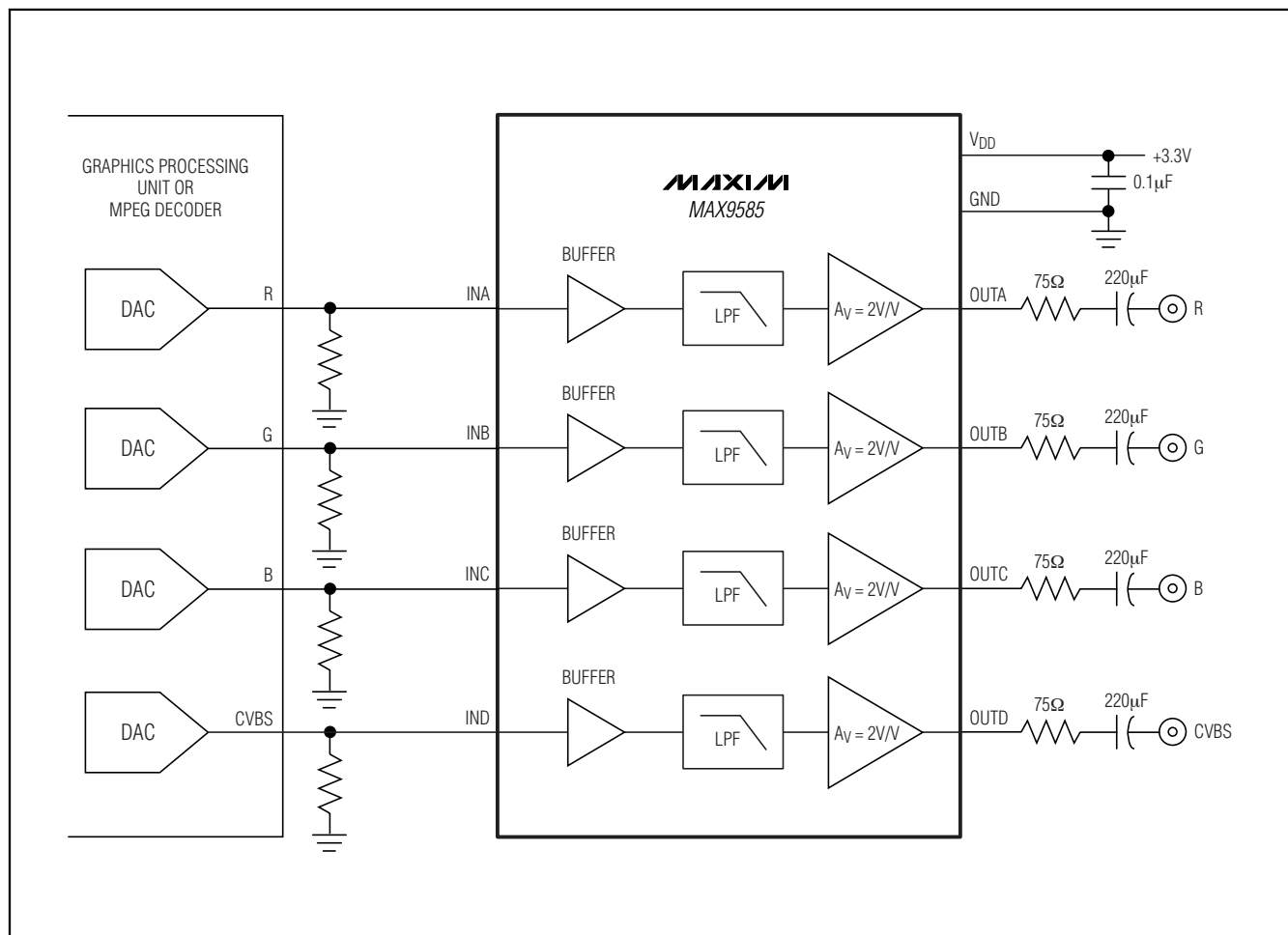
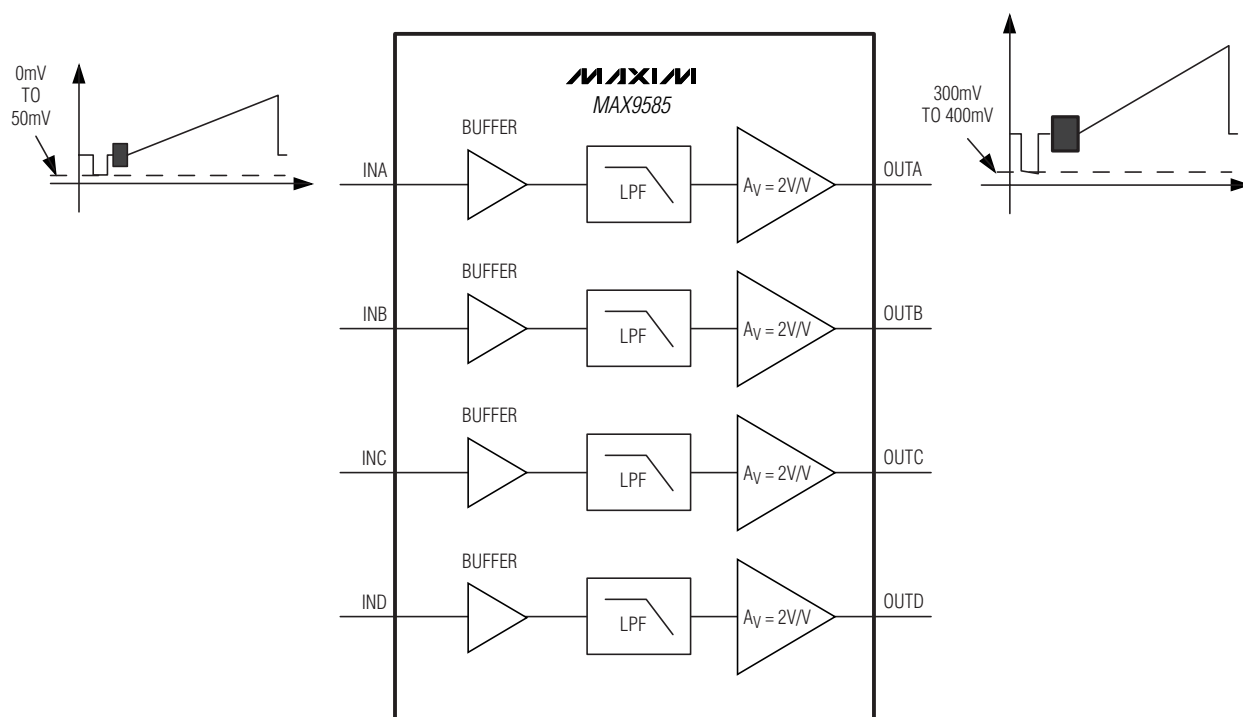
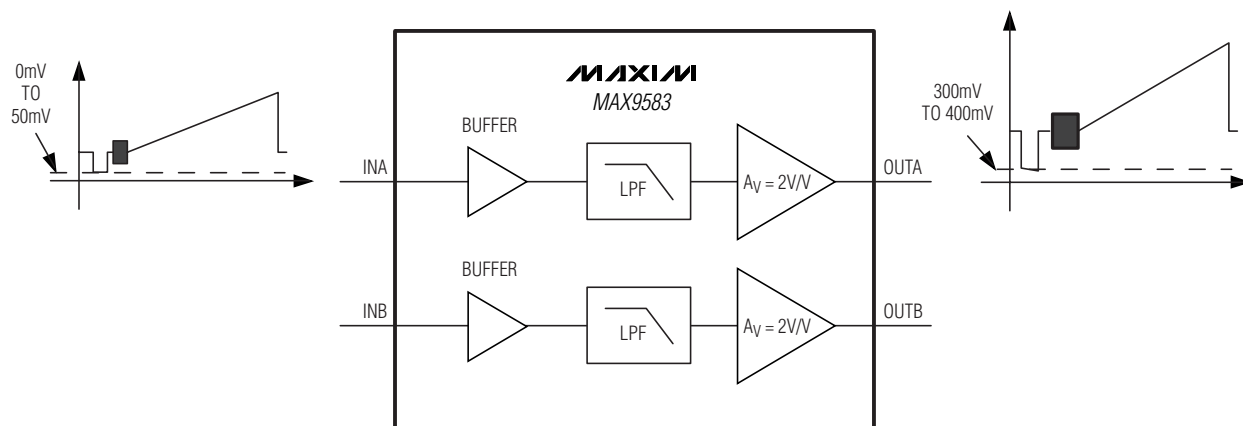


Figure 1. AC-Coupled Outputs

# Dual, Triple, and Quad Standard-Definition Video Filter Amplifiers with DC-Coupled Input Buffers

## Block Diagrams (continued)

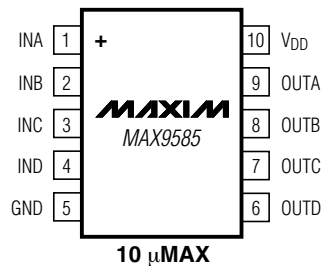
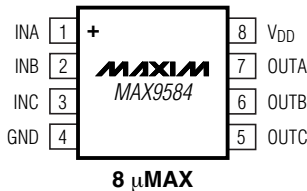
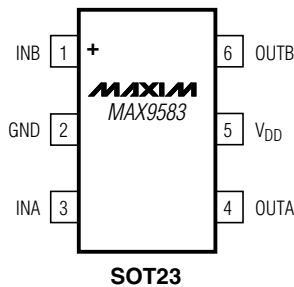


# Dual, Triple, and Quad Standard-Definition Video Filter Amplifiers with DC-Coupled Input Buffers

## Pin Configurations

MAX9583/MAX9584/MAX9585

TOP VIEW



## Selector Guide

| PART        | PIN-PACKAGE    | PACKAGE SIZE  | CHANNELS | TOP MARK | PKG CODE |
|-------------|----------------|---------------|----------|----------|----------|
| MAX9583AZT+ | 6 Thin SOT23-6 | 2.9mm x 1.6mm | 2        | AADJ     | Z6+1     |
| MAX9584AUA+ | 8 μMAX-8       | 3mm x 3mm     | 3        | —        | U8+1     |
| MAX9585AUB+ | 10 μMAX-10     | 3mm x 3mm     | 4        | —        | U10+2    |

**Note:** All devices are specified over the -40°C to +125°C operating temperature range.  
+Denotes a lead-free package.

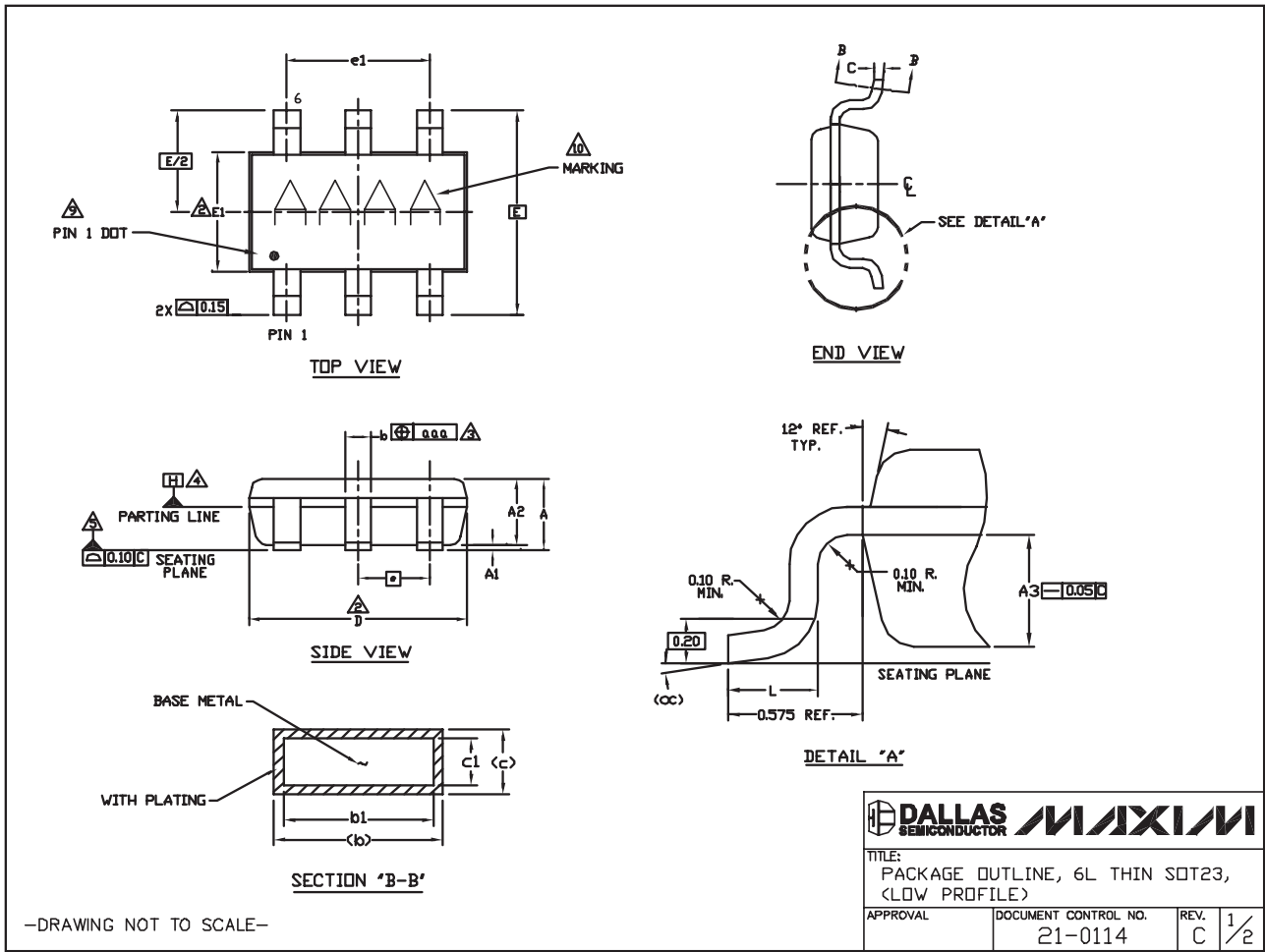
## Chip Information

PROCESS: BICMOS

# Dual, Triple, and Quad Standard-Definition Video Filter Amplifiers with DC-Coupled Input Buffers

## Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to [www.maxim-ic.com/packages](http://www.maxim-ic.com/packages).)



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## Package Information (continued)

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### NOTES:

1. ALL DIMENSIONS ARE IN MILLIMETERS.

2. "D" AND "E1" ARE REFERENCE DATUM AND DO NOT INCLUDE MOLD FLASH OR PROTRUSIONS, AND ARE MEASURED AT THE BOTTOM PARTING LINE. MOLD FLASH OR PROTRUSION SHALL NOT EXCEED 0.15mm ON "D" AND 0.25mm ON "E" PER SIDE.

3. THE LEAD WIDTH DIMENSION DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.07mm TOTAL IN EXCESS OF THE LEAD WIDTH DIMENSION AT MAXIMUM MATERIAL CONDITION.

4. DATUM PLANE "H" LOCATED AT MOLD PARTING LINE AND COINCIDENT WITH LEAD, WHERE LEAD EXITS PLASTIC BODY AT THE BOTTOM OF PARTING LINE.

5. THE LEAD TIPS MUST LIE WITHIN A SPECIFIED TOLERANCE ZONE. THIS TOLERANCE ZONE IS DEFINED BY TWO PARALLEL LINES. ONE PLANE IS THE SEATING PLANE, DATUM [-C-J] AND THE OTHER PLANE IS AT THE SPECIFIED DISTANCE FROM [-C-J] IN THE DIRECTION INDICATED. FORMED LEADS SHALL BE PLANAR WITH RESPECT TO ONE ANOTHER WITH 0.10mm AT SEATING PLANE.

6. THIS PART IS COMPLIANT WITH JEDEC SPECIFICATION MO-193 EXCEPT FOR THE "e" DIMENSION WHICH IS 0.95mm INSTEAD OF 1.00mm. THIS PART IS IN FULL COMPLIANCE TO EIAJ SPECIFICATION SC-74.

7. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS. COPLANARITY SHALL NOT EXCEED 0.08mm.

8. WARPAGE SHALL NOT EXCEED 0.10mm.

9. THE TERMINAL #1 IDENTIFIER AND TERMINAL NUMBERING CONVENTION SHALL CONFORM TO JEDEC 95-1 PP-012. DETAILS OF TERMINAL #1 IDENTIFIER ARE OPTIONAL. THE TERMINAL #1 IDENTIFIER MAY BE EITHER A MOLD OR MARKED FEATURE.

10. MARKING IS FOR PACKAGE ORIENTATION REFERENCE ONLY.

11. ALL DIMENSIONS APPLY TO BOTH LEADED (-) AND LEAD FREE (+) PACKAGE CODES.

### SYMBOLS

|                       | MIN      | NOM   | MAX  |
|-----------------------|----------|-------|------|
| A                     | —        | —     | 1.10 |
| A1                    | 0.00     | 0.075 | 0.10 |
| A2                    | 0.85     | 0.88  | 0.90 |
| A3                    | 0.50 BSC |       |      |
| b                     | 0.30     | —     | 0.45 |
| b1                    | 0.25     | 0.35  | 0.40 |
| c                     | 0.15     | —     | 0.20 |
| c1                    | 0.12     | 0.127 | 0.15 |
| D                     | 2.80     | 2.90  | 3.00 |
| E                     | 2.75 BSC |       |      |
| E1                    | 1.55     | 1.60  | 1.65 |
| L                     | 0.30     | 0.40  | 0.50 |
| e1                    | 1.90 BSC |       |      |
| e                     | 0.95 BSC |       |      |
| OC                    | 0°       | 4°    | 8°   |
| aaa                   | 0.20     |       |      |
| Pkg. codes: Z6-1 Z6-2 |          |       |      |

-DRAWING NOT TO SCALE-

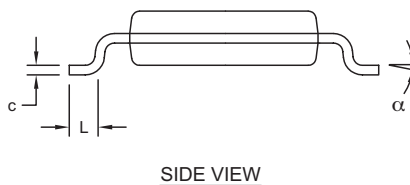
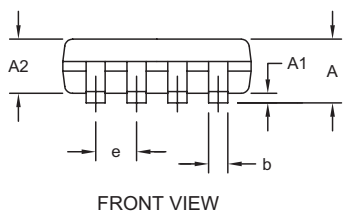
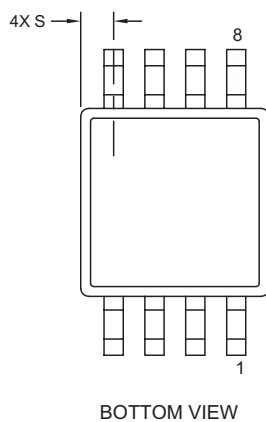
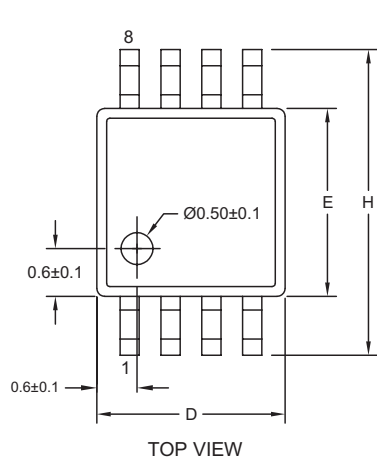
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|---------------------------------------------------------------------------------------|----------------------|------|-----|
|  |                      |      |     |
| TITLE:<br>PACKAGE OUTLINE, 6L THIN SOT23,<br>(LOW PROFILE)                            |                      |      |     |
| APPROVAL                                                                              | DOCUMENT CONTROL NO. | REV. | 2/2 |
|                                                                                       | 21-0114              | C    |     |

MAX9583/MAX9584/MAX9585

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## Package Information (continued)

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| DIM | INCHES     |       | MILLIMETERS |      |
|-----|------------|-------|-------------|------|
|     | MIN        | MAX   | MIN         | MAX  |
| A   | -          | 0.043 | -           | 1.10 |
| A1  | 0.002      | 0.006 | 0.05        | 0.15 |
| A2  | 0.030      | 0.037 | 0.75        | 0.95 |
| b   | 0.010      | 0.014 | 0.25        | 0.36 |
| c   | 0.005      | 0.007 | 0.13        | 0.18 |
| D   | 0.116      | 0.120 | 2.95        | 3.05 |
| e   | 0.0256 BSC |       | 0.65 BSC    |      |
| E   | 0.116      | 0.120 | 2.95        | 3.05 |
| H   | 0.188      | 0.198 | 4.78        | 5.03 |
| L   | 0.016      | 0.026 | 0.41        | 0.66 |
| α   | 0°         | 6°    | 0°          | 6°   |
| S   | 0.0207 BSC |       | 0.5250 BSC  |      |

### NOTES:

1. D & E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED 0.15MM (.006").
3. CONTROLLING DIMENSION: MILLIMETERS.
4. MEETS JEDEC MO-187C-AA.

|                                                                                                                      |                      |                                                                                       |    |
|----------------------------------------------------------------------------------------------------------------------|----------------------|---------------------------------------------------------------------------------------|----|
|  <b>DALLAS</b><br>SEMICONDUCTOR |                      |  |    |
| PROPRIETARY INFORMATION                                                                                              |                      |                                                                                       |    |
| TITLE:                                                                                                               |                      |                                                                                       |    |
| PACKAGE OUTLINE, 8L uMAX/uSOP                                                                                        |                      |                                                                                       |    |
| APPROVAL                                                                                                             | DOCUMENT CONTROL NO. | REV.                                                                                  | 1/ |
|                                                                                                                      | 21-0036              | J                                                                                     |    |

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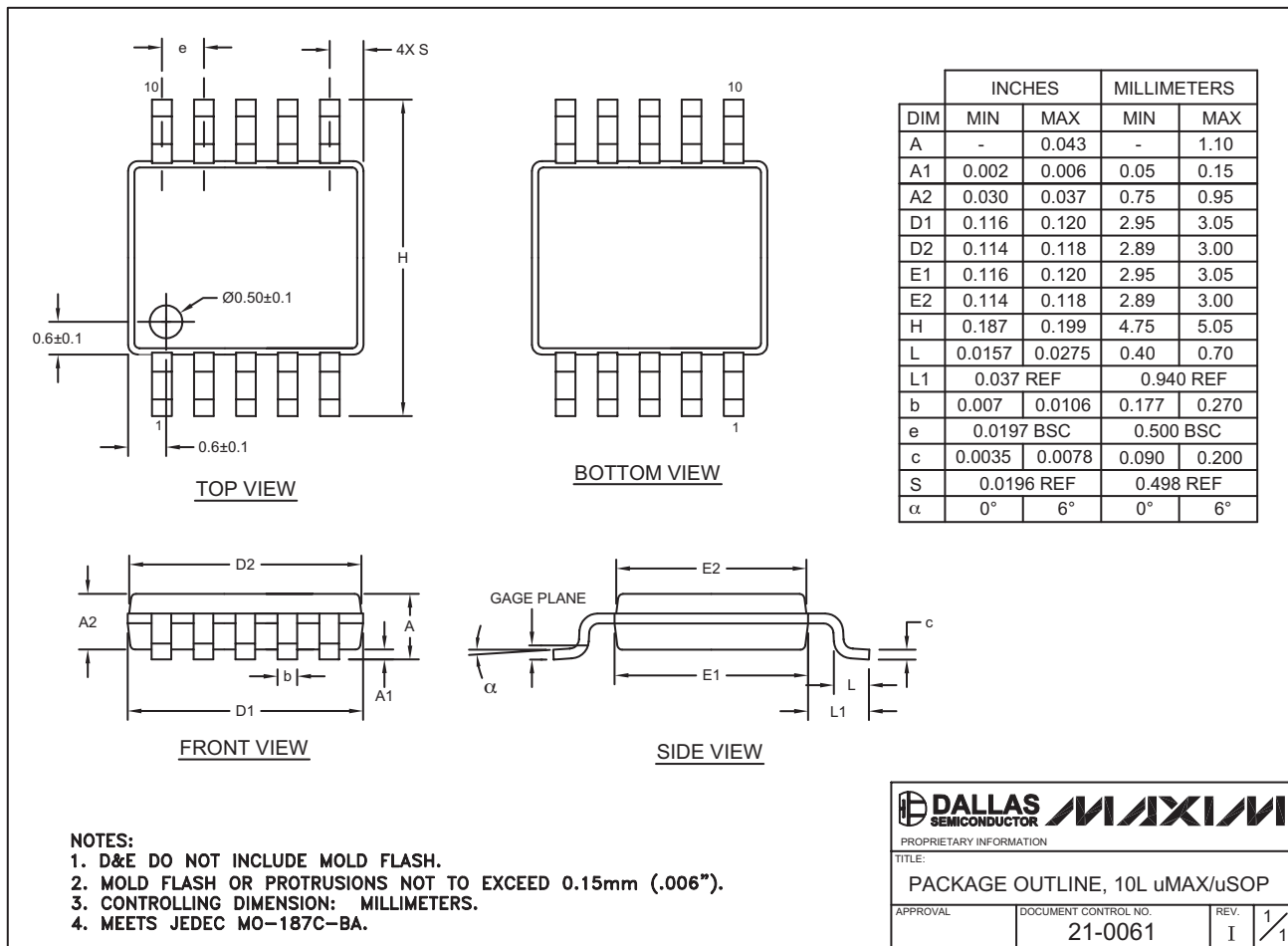
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MAX9583/MAX9584/MAX9585

10L uMAX/EPs



## Revision History

Pages changed at Rev 1: 1, 2, 20

Pages changed at Rev 2: 1, 2, 6, 11-15 (deleted some package outlines)

Pages changed at Rev 3: 1, 15

Pages changed at Rev 4: 1-4, 8, 15

Pages changed at Rev 5: 1, 8, 15

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